

FORM PTO-1449 (modified)
To: U.S. Department of Commerce
(PM&S FORM PAT-1449)
Patent and Trademark Office

Atty.
Dkt. No.

M#

Client Ref.

251017

Applicant: ELLIS III, Frampton

Appln. No.: 09/320,660

Filing Date: May 7, 1999

Examiner: D. Dinh

Group A

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date: April 27, 2000

Page

1

of

1

U.S. PATENT DOCUMENTS

Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
	AR 5,864,738	01/1999	Kessler et al.	709	23	
	BR 5,793,968	08/1998	Gregerson et al.	709	20	
	CR 5,909,681	06/1999	Passera et al.	707	8	
	DR 5,572,643	11/1996	Judson	709	48	
	ER 5,590,284	12/1996	Crosetto	712	29	
	FR 5,930,511	07/1999	Hinsley	717	10	
	GR 5,978,829	11/1999	Chung et al.	709	102	
	HR 5,748,489	05/1998	Beatty et al.	716	7	
	IR 5,696,902	12/1997	Leclercq et al.	705	48	
	JR 5,349,682	09/1994	Rosenberry	709	402	
	KR					
	LR					
	MR					

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract	Translation Readily Available
	NR 0 647 052 A1	09/1994	European	Leclercq, Thierry	Enclosed	No
	OR				No	Enclose
	PR				No	No

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

QR	Geoffrey C. Fox and Wojtek Furmanski, Petaops and Exaops: Supercomputing on the Web, "IEEE INTERNET COMPUTING," Vol. 1, Number 2 March-April 1997, pgs. 38-46.				
RR	Kivanc Dincer and Geoffrey C. Fox, Building a World-Wide Virtual Machine Based on Web and HPCC Technologies, "STUDENT TECHNICAL PAPERS," http://www.supercomp.org/sc96/proceedings/SC96PROC/DINCER/INDEX.HTM pgs. 1-18.				
SR	M. Hobbs and A. Goscinski, A Remote Porcess Creation and Execution Facility Supporting Parallel Execution on Distributed Systems, "IEEE 1996," pgs. 92-99.				
TR	Taisuke Boku, Hiroshi Nakamura, Kisaburo Nakazawa, and Yoichi Iwasaki, The Architecture of Massively Parallel Processor CP-PACS, "IEEE 1997," pgs. 31-40.				
UR	Yoon-Hwa Choi and Yu-Seok Kim, A Diagnostic Network for Massively Parallel Processing Systems, "IEEE 1994," pgs. 348-353.				
VR	Steven M. Bellovin and William R. Cheswick, Network Firewalls, "IEEE Communications Magazine 1994," pgs. 50-57.				

Examiner

D. Dinh

Date Considered:

6/2/00

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.